

# **EXHIBIT 22**

**IN THE UNITED STATES DISTRICT COURT  
FOR THE WESTERN DISTRICT OF TEXAS  
MIDLAND-ODESSA DIVISION**

REDSTONE LOGICS LLC,

Plaintiff,

v.

QUALCOMM INC.; QUALCOMM  
TECHNOLOGIES, INC.,

Defendants.

Case No. 7:24-cv-00231-ADA

JURY TRIAL DEMANDED

**JOINT CLAIM CONSTRUCTION STATEMENT**

Pursuant to the Court’s Scheduling Order (Dkt. 24), the Parties submit this Joint Claim Construction Statement. The claim terms identified below as agreed upon or being in dispute are found in U.S. Patent No. 8,549,339 (Dkt. 1-1).

**LIST OF DISPUTED TERMS**

	<b>Claim Term</b>	<b>Claims</b>	<b>Redstone’s Proposed Construction</b>	<b>Qualcomm’s Proposed Construction</b>
1.	“the first clock signal is independent from the second clock signal” [Qualcomm Term]	1, 21	Plain and ordinary meaning	Plain and ordinary, which requires that the first and second clock signals depend from different reference oscillator clocks
2.	“located in a periphery of the multi-core processor” [Qualcomm Term]	5	Plain and ordinary meaning	Indefinite
3.	“located in a common region that is substantially central to the first set of processor cores and the second set of processor cores” [Qualcomm Term]	14	“Located in a common region of the first set of processor cores and the second set of	Indefinite

	<b>Claim Term</b>	<b>Claims</b>	<b>Redstone's Proposed Construction</b>	<b>Qualcomm's Proposed Construction</b>
			processor cores"	

**LIST OF AGREED UPON TERMS**

	<b>Claim Term</b>	<b>Claims</b>	<b>Agreed Upon Construction</b>
4.	"a first/second set of processor cores,"	1, 21	"a first/second group of two or more processor cores"

Dated: May 29, 2025

Respectfully submitted,

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By: /s/ Richard S. Zembek

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**CERTIFICATE OF SERVICE**

I hereby certify that counsel of record who are deemed to have consented to electronic service are being served this May 29, 2025 with a copy of this document via the Court's CM/ECF System.

/s/ Joshua Scheufler